

Electrical and Computer Engineering Department

The Program

The Master of Science in Engineering (MSE) provides students with advanced engineering education in Electrical or Computer Engineering degree options. The overarching goal of the program is to enhance the students' ability to be successful and advance in their chosen careers in industry, academia, and public instruction.



Overview

The program provides opportunities for integration and cross areas of electrical and computer engineering students to do research and project in the following areas:

- Higher Frequency Electronics
- Communications
- Control Systems and Robotics
- Power Electronics
- VLSI System Design
- Computer Architecture and Embedded Systems

Graduate Studies

MSE Electrical Engineering Option

MSE Computer Engineering Option

The Goals of the Graduate Program

1. Enhance the students' understanding of advanced theoretical and practical concepts in engineering.
2. Allow students to achieve an appropriate level of competence in solving practical problems.
3. Develop students' critical thinking skills necessary for mastering new topics related to the advances in engineering.
4. Allow students to acquire substantive knowledge that will permit them to pursue advanced academic or industrial work.

California State University

Lyles College of Engineering
Electrical and Computer
Engineering Department
2340 San Ramon AVE EE 94
Fresno, CA 93740



Degree Requirements and Faculty Research Area

Research and Projects

Faculty in the Department of Electrical and Computer Engineering are engaged in a variety of research and development activities. The following are the general areas of research. Contact individual faculty members for additional information.

- **Dr. Nagy Bengiamin** at bengiami@csufresno.edu Control Systems, Power Systems, Robotics
- **Dr. Daniel Bukofzer** at danielbu@csufresno.edu Signal Processing, Wireless Communications
- **Dr. Ram Nunna** at rnunna@csufresno.edu VLSI, Computer Architecture, Digital Systems
- **Dr. Reza Raeisi** at rreisi@csufresno.edu : Embedded Systems, VLSI Testing, Design for Testability
- **Dr. Gregory Kriehn** at gkriehn@csufresno.edu Optoelectronics, Optical Signal Processing
- **Dr. Zoulikha Mouffak** at zmouffak@csufresno.edu Solid State Devices, Semiconductor IC Process Technology
- **Dr. Young Kim** at youngkim@csufresno.edu RF Electronics, Microwave
- **Dr. Charles Won** at chwon@csufresno.edu Computer Networks, Wireless Sensor Networks
- **Dr. Peter Kinman** at pkinman@csufresno.edu Communication and Signal Processing
- **Dr. Albert Heaney** at aheaney@csufresno.edu Digital Systems



Degree Requirements

MS Engineering 30 Units Program		
Required Core Courses	Electrical Engr. Option (10-units): ENGR 200; ENGR 201; ECE 224; One course from ECE 230, ECE 241, ENGR 206	Computer Engr. Option (10-units): ENGR 200, ENGR 201, ECE 278, one course from ECE 240, ECE 242, ECE 274
Thesis Option	<ul style="list-style-type: none"> • Major courses: 6 units (select from the 200-level ECE courses) • Elective Courses 8 units (select from ECE 200-level courses, 100-level ECE courses, and other 200-level courses approved by graduate coordinator) • Thesis 6 units (ECE 299) 	
Project Option	<ul style="list-style-type: none"> • Major courses: 6 units (select from the 200-level ECE courses) • Elective Courses 8 units (select from ECE 200-level courses, 100-level ECE courses, and other 200-level courses approved by graduate coordinator) • Project 3 units (ECE 298) 	
Exam Option	<ul style="list-style-type: none"> • Major courses: 6 units (select from the 200-level ECE courses) • Elective Courses 8 units (select from ECE 200-level courses, 100-level ECE courses, and other 200-level courses approved by graduate coordinator) • Comprehensive Exam: 0 unit 	

For complete admission requirements, deadlines and procedures to apply refer to the [Division of Graduate Studies and International Student Service and Programs](#) at California State University, Fresno at the following links:

<http://www.fresnostate.edu/academics/gradstudies/>

<http://www.fresnostate.edu/studentaffairs/issp/>

For departmental detail requirements contact the graduate program coordinator:

Reza Raeisi, PhD

Professor and Graduate Program Coordinator

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Phone: (559) 278-6038

Email: rreisi@csufresno.edu

Website: <http://www.fresnostate.edu/engineering/elec-computer/grad-programs/>